

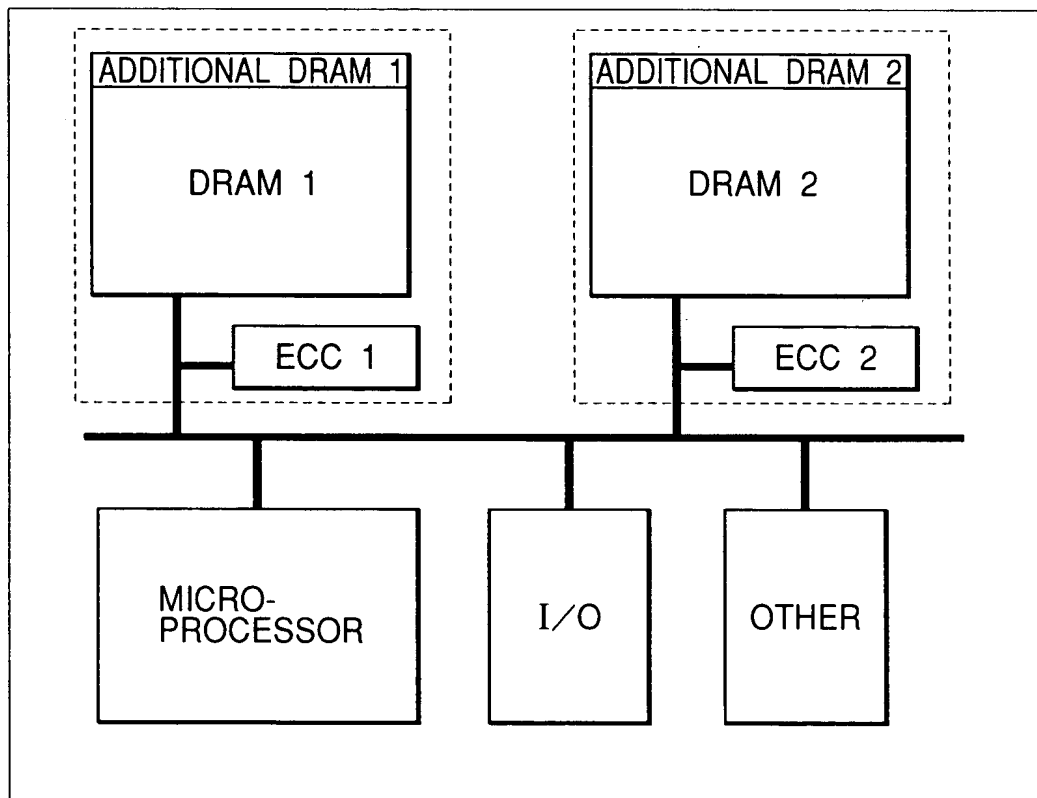
FIG. 1

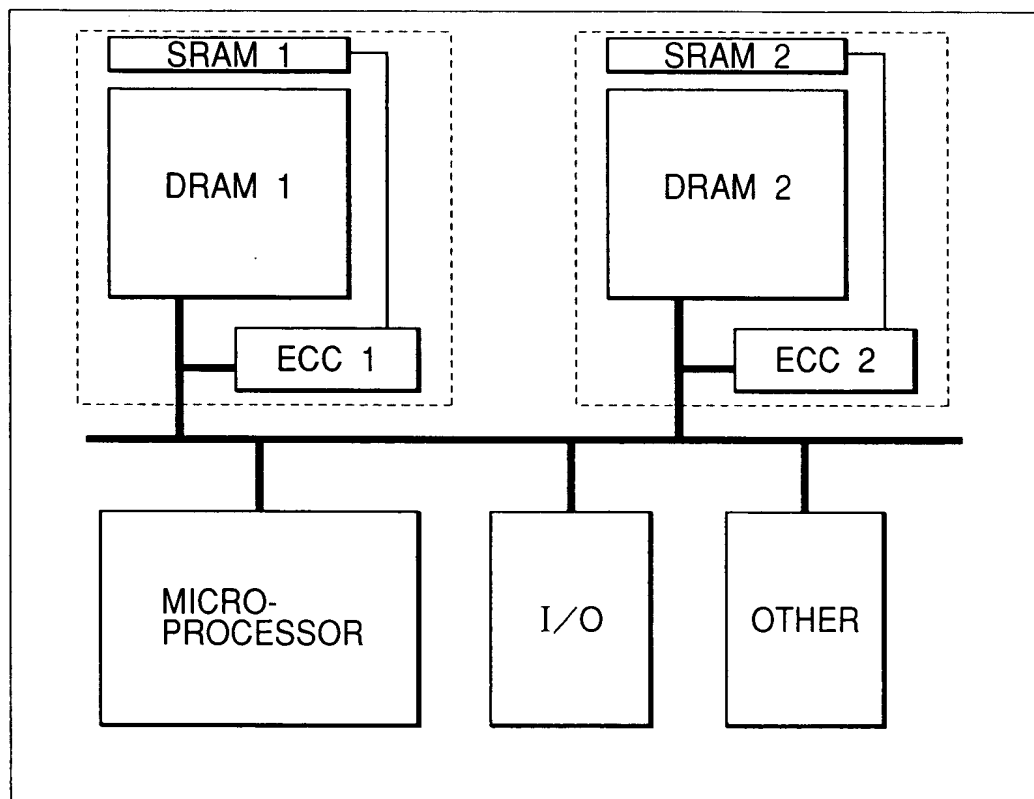
FIG. 2

FIG. 3

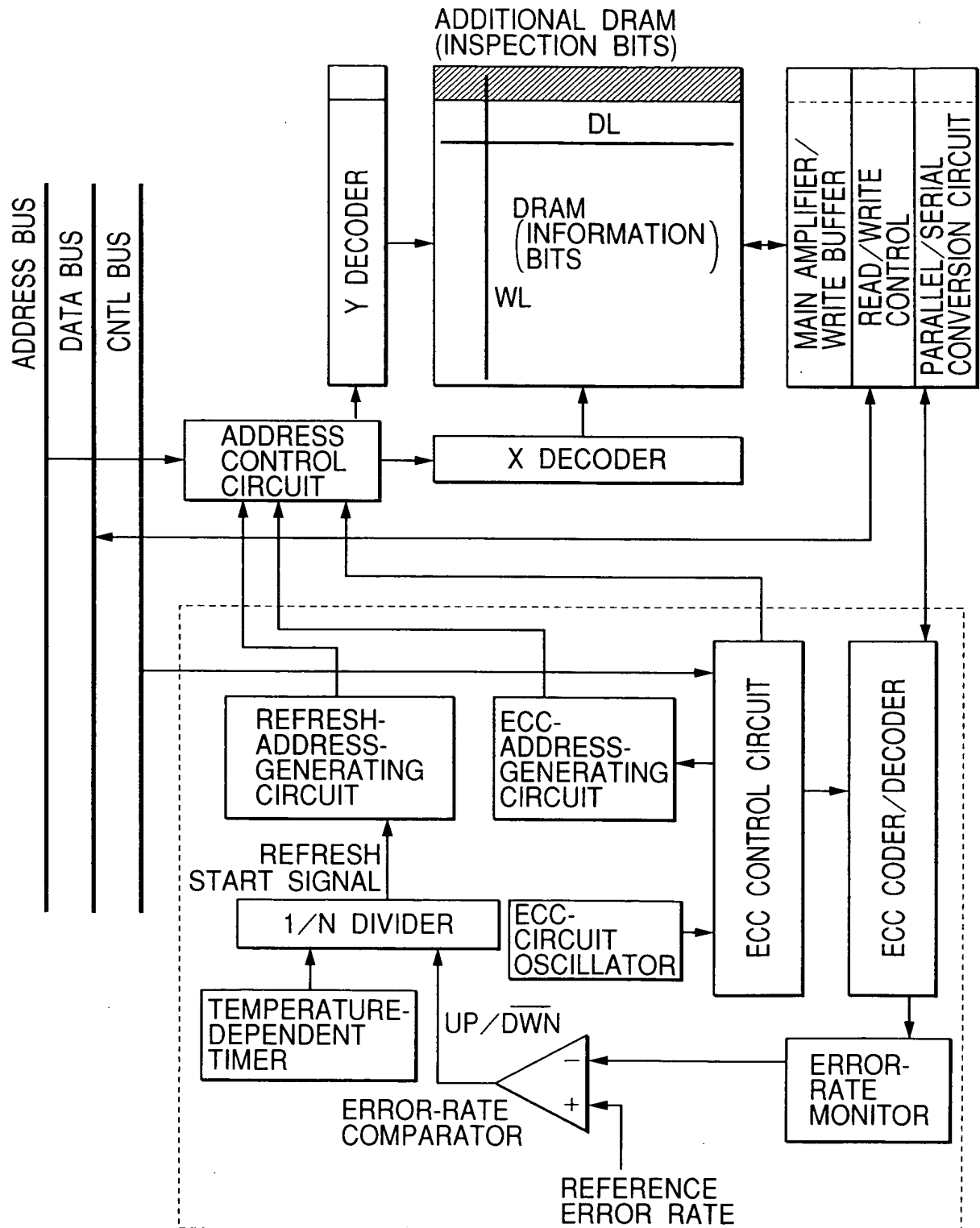


FIG. 4

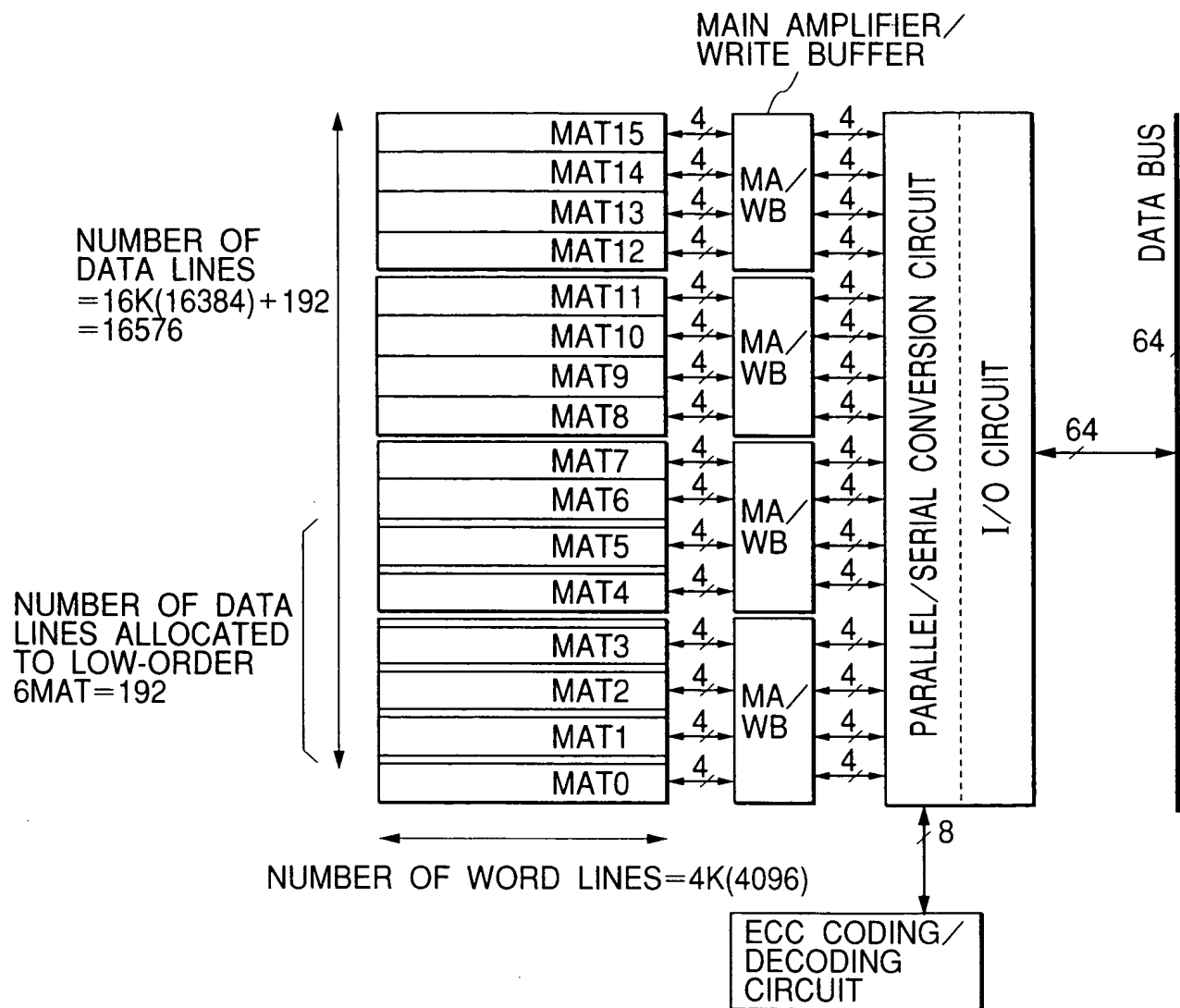
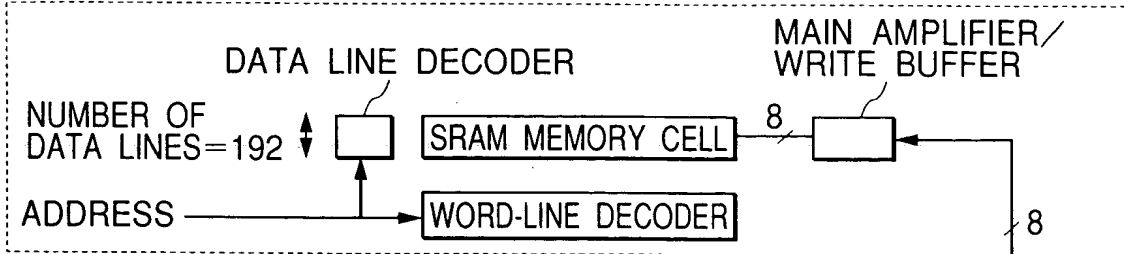


FIG. 5

SRAM MOCRO UNIT



DRAM MOCRO UNIT

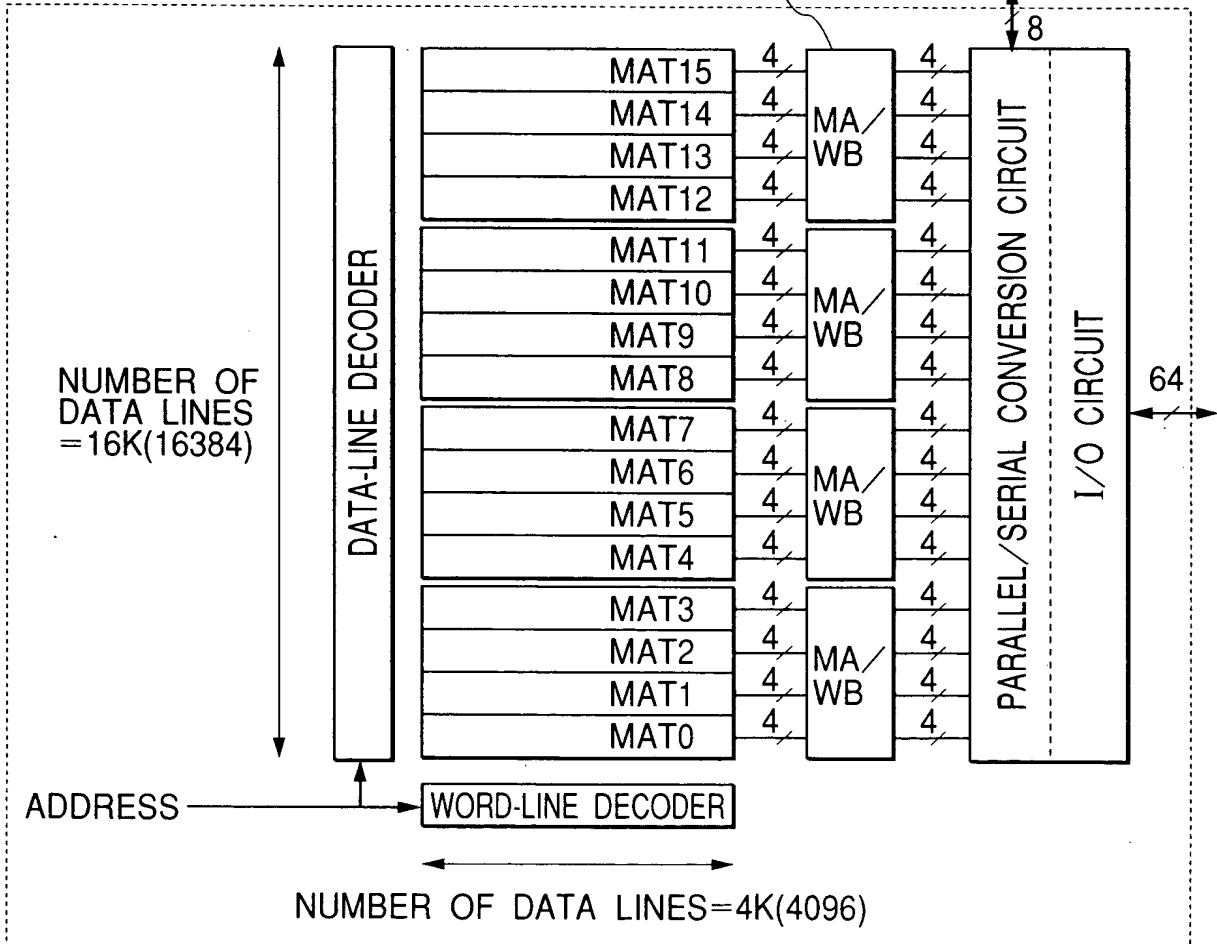


FIG. 6

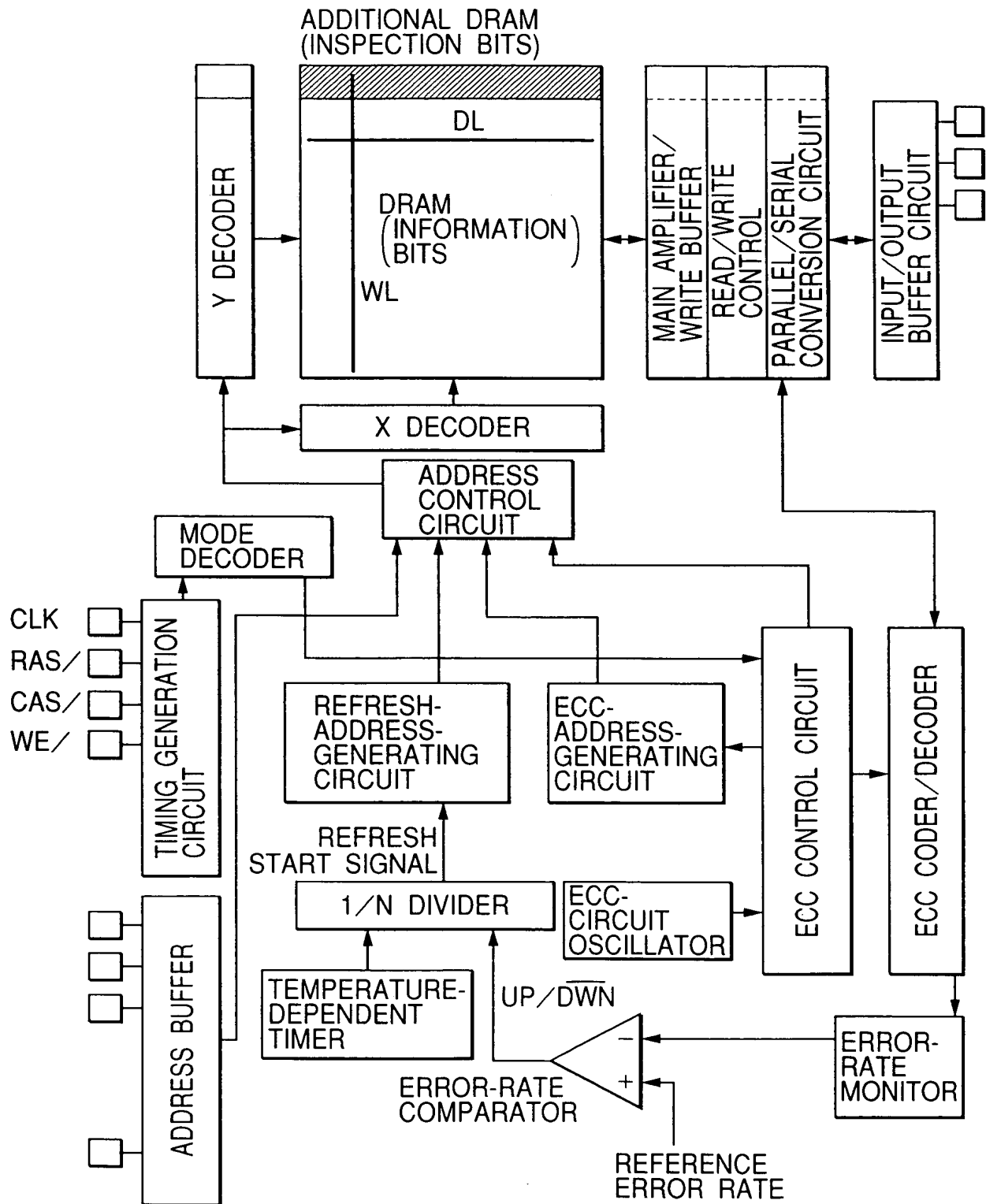


FIG. 7

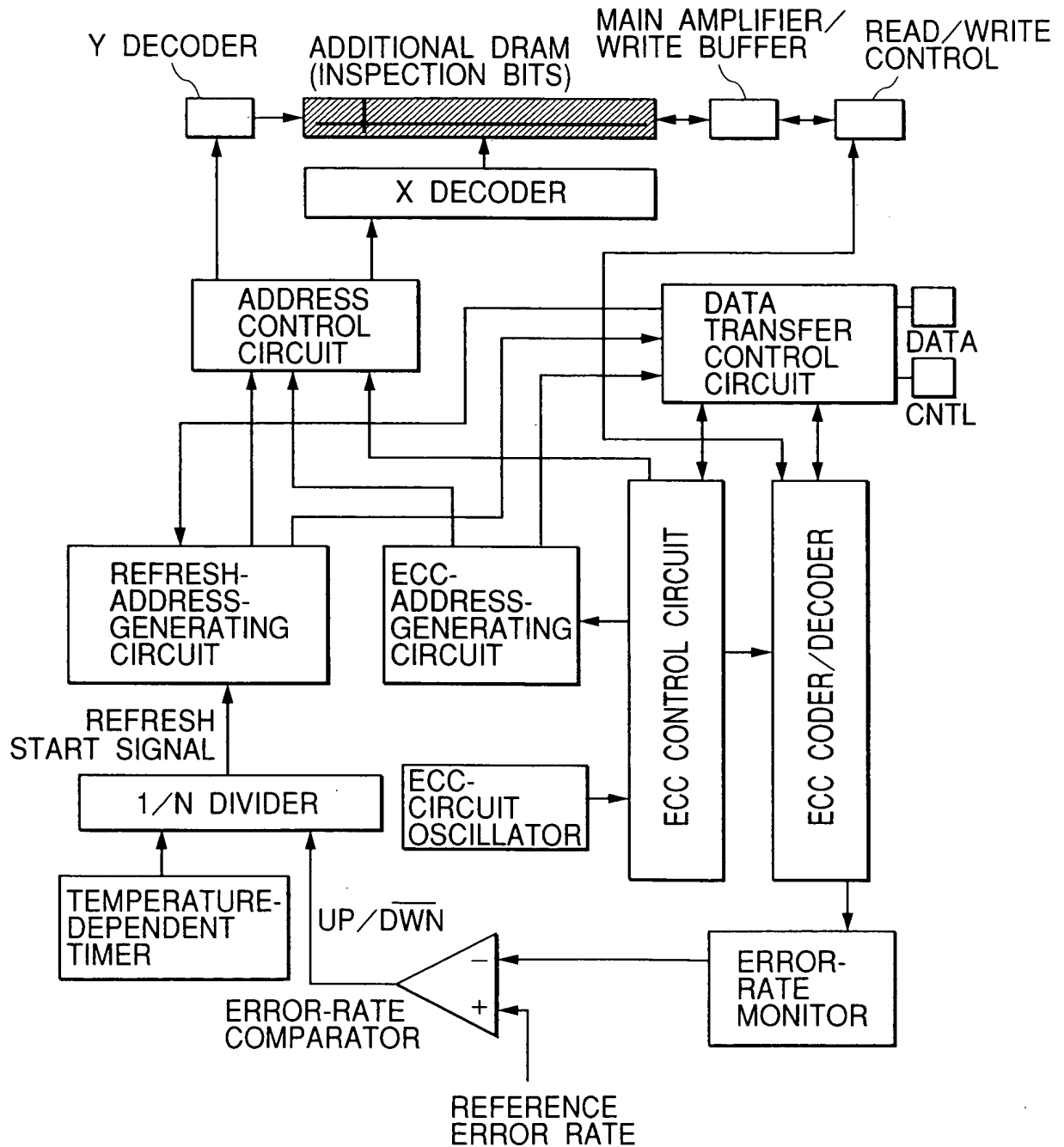


FIG. 8

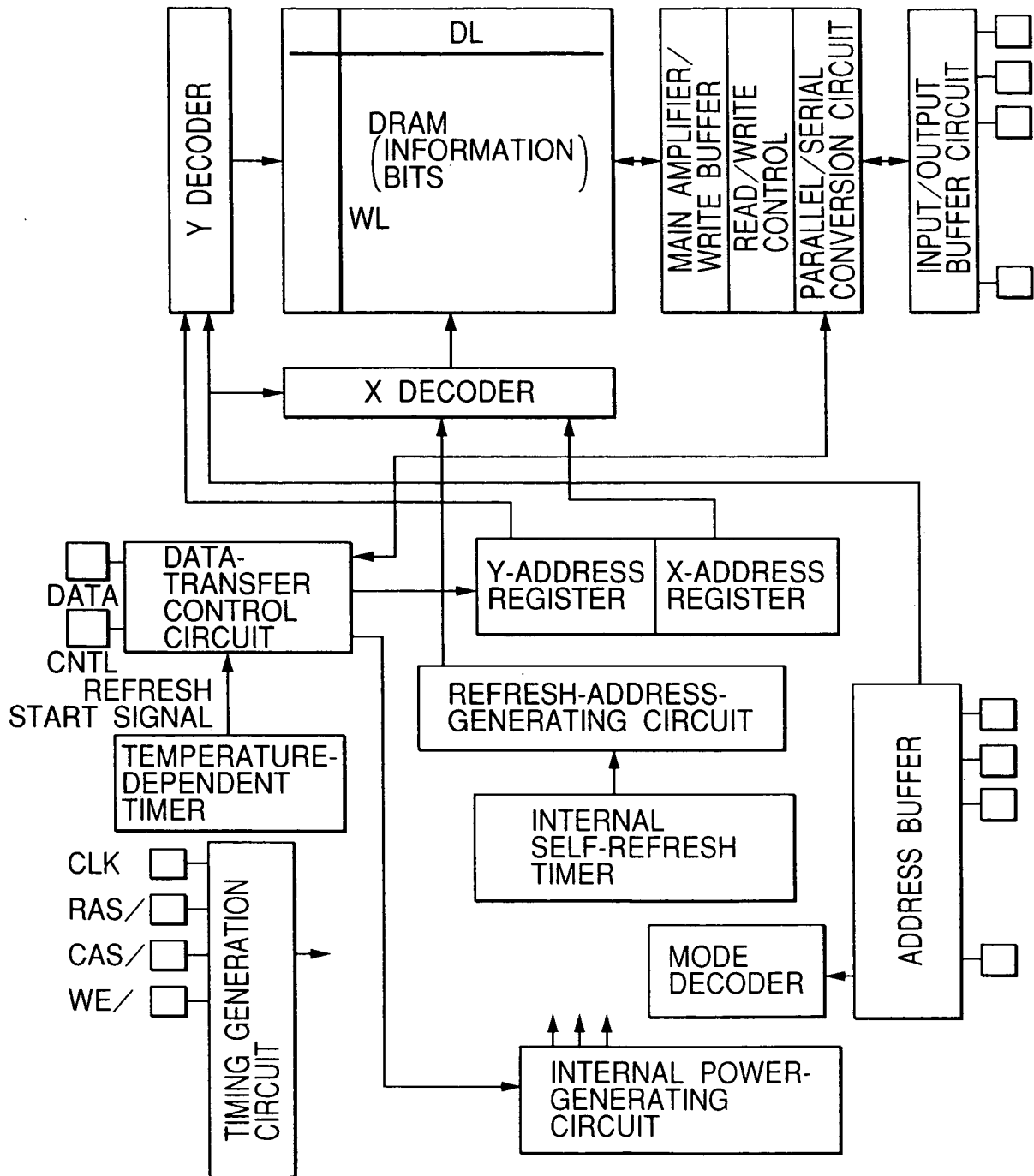


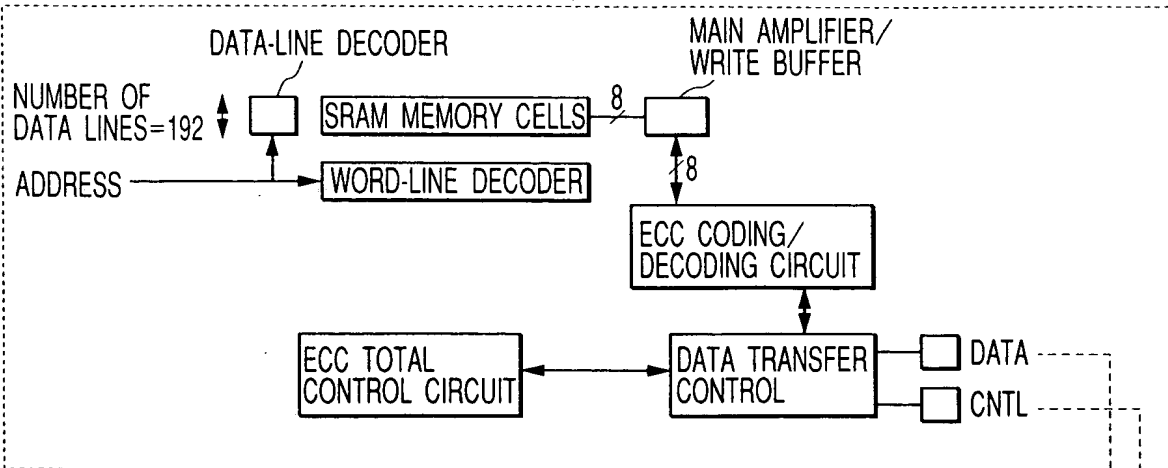
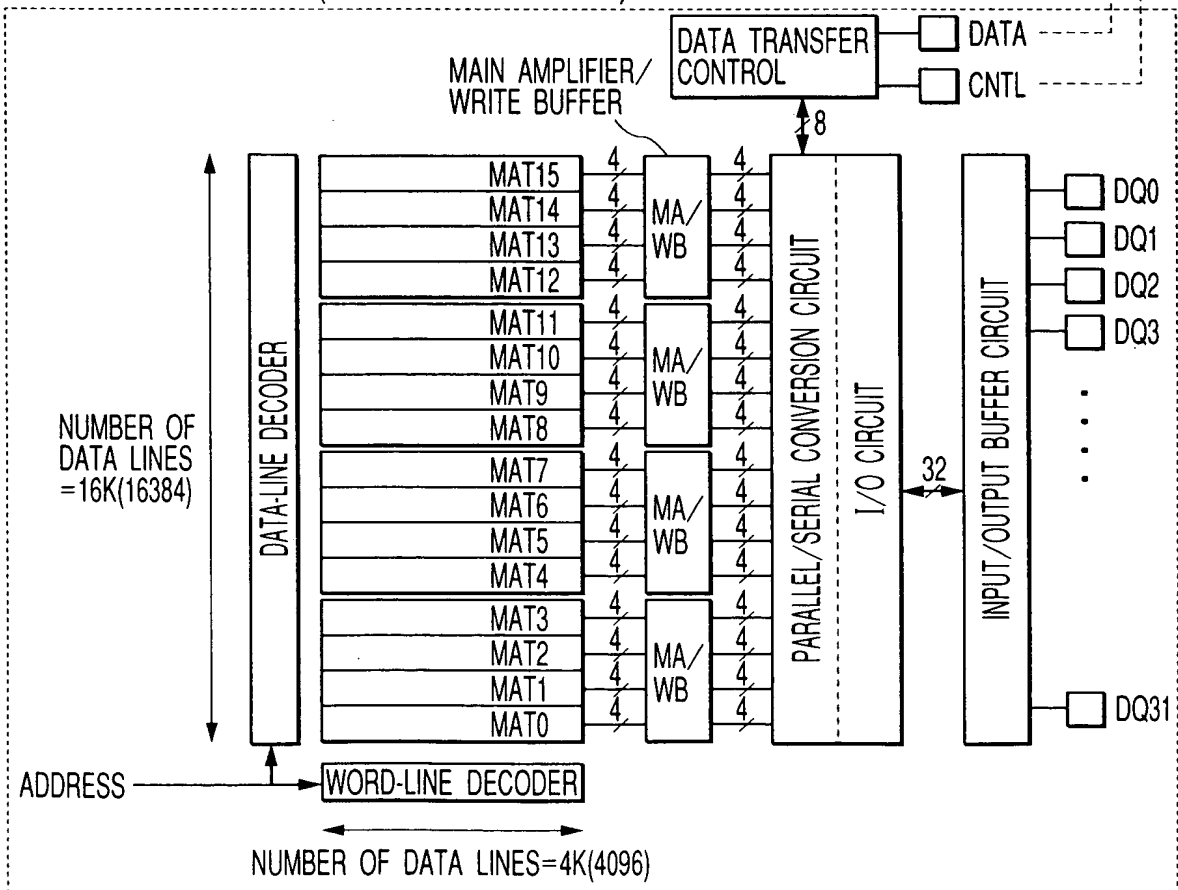
FIG. 9**EXTERNAL ECC (INSPECTION BITS) UNIT****MAIN-BODY DRAM (INFORMATION BIT) UNIT**

FIG. 10

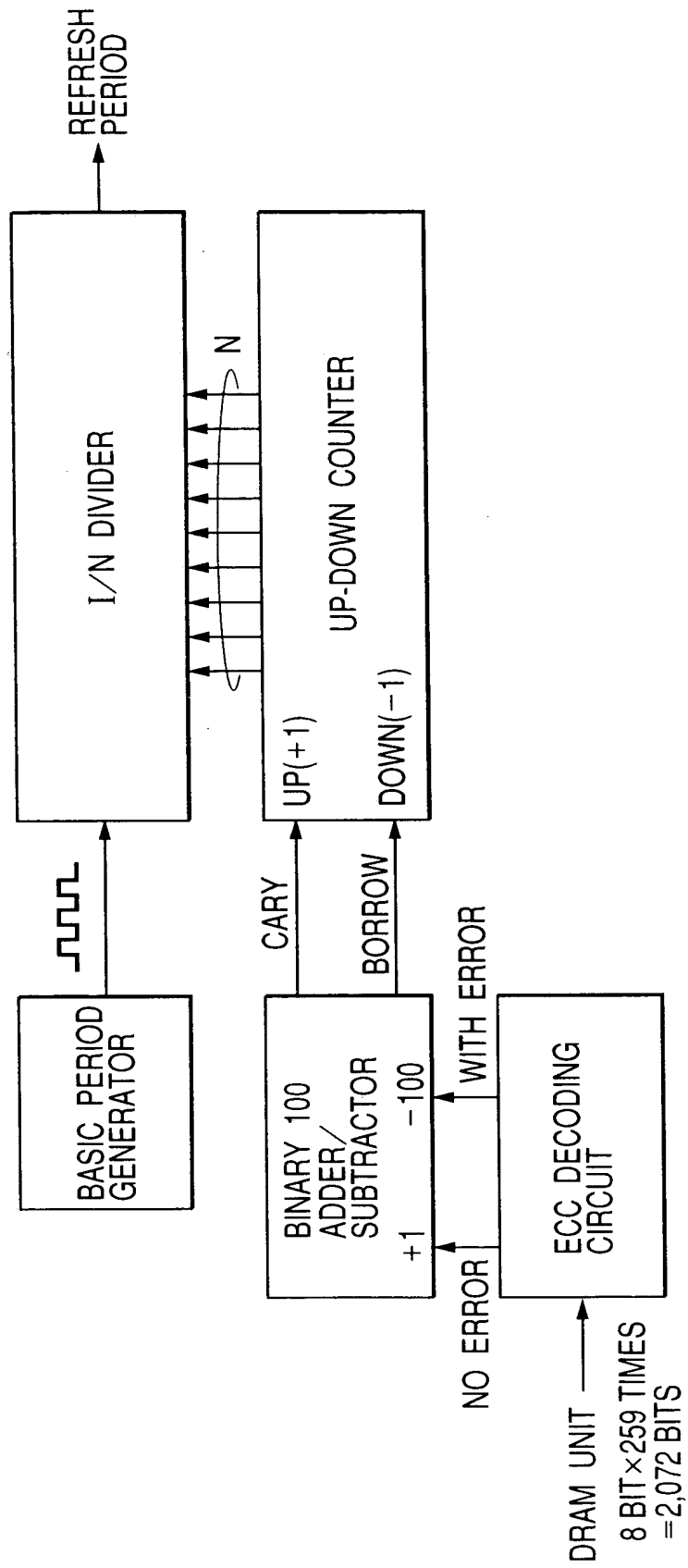


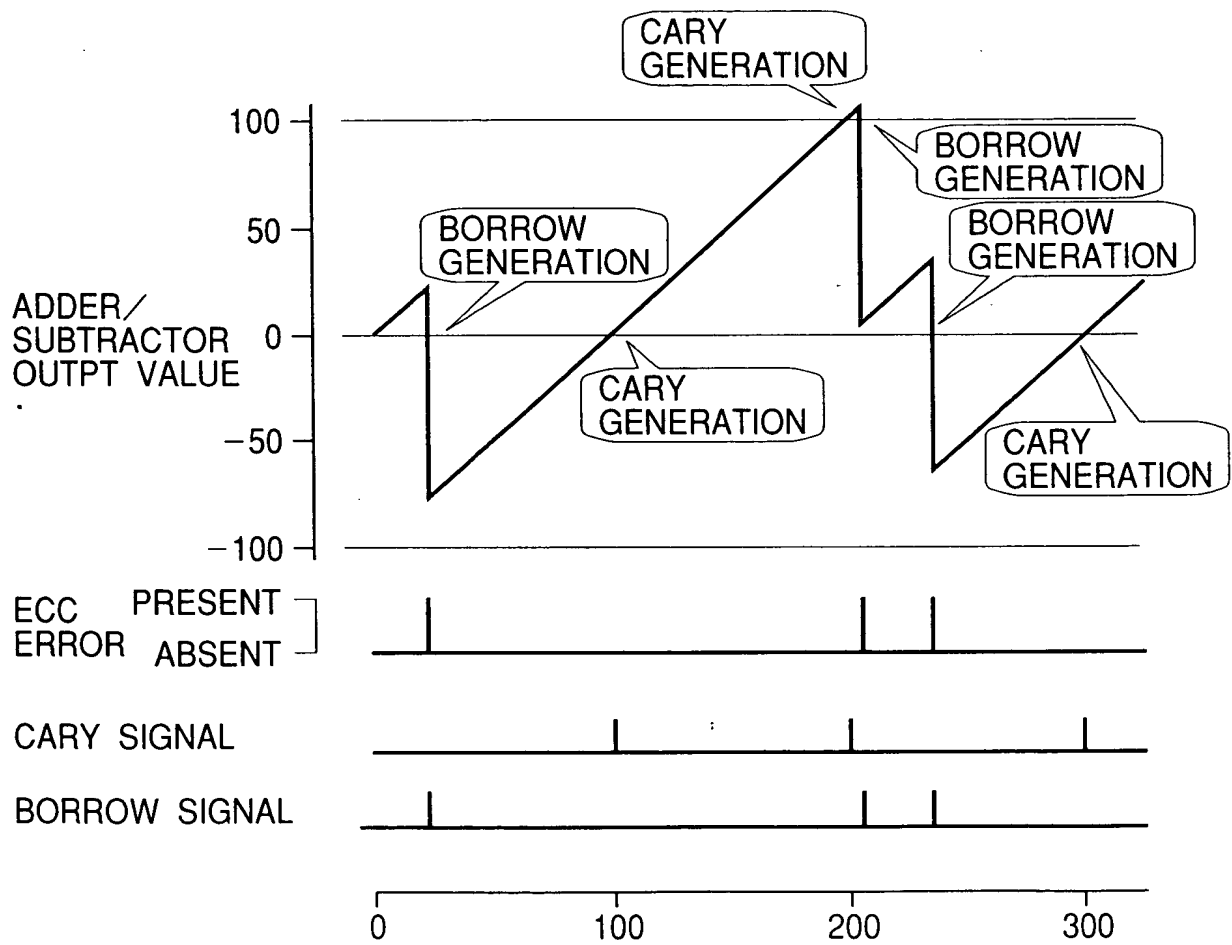
FIG. 11

FIG. 12

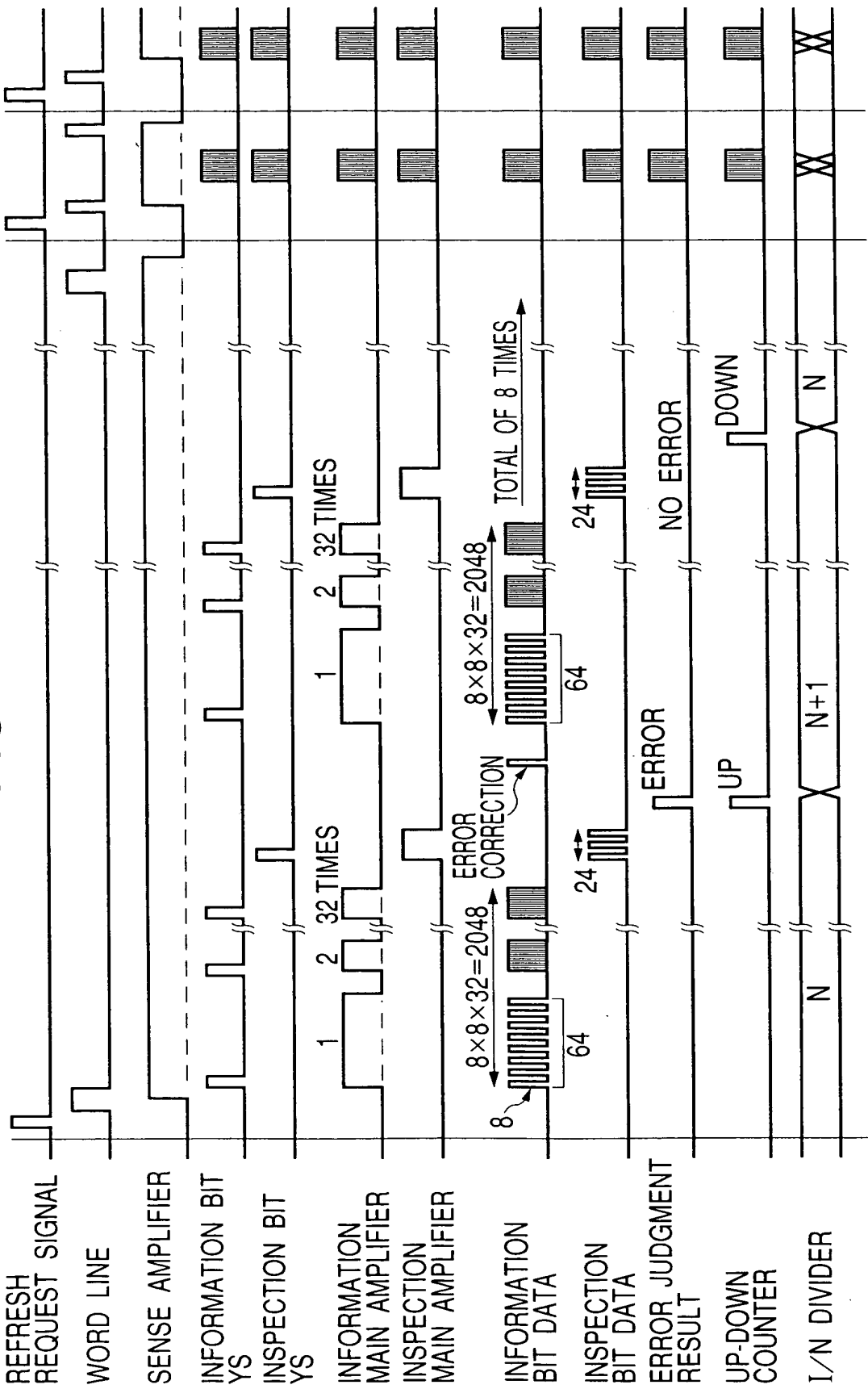


FIG. 13

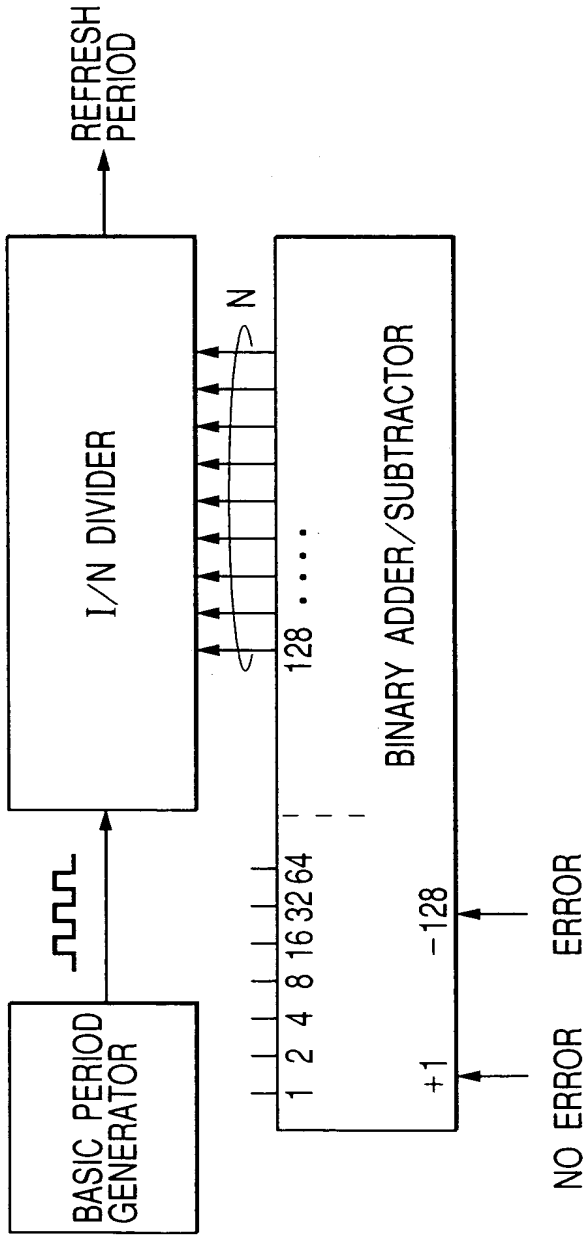


FIG. 14

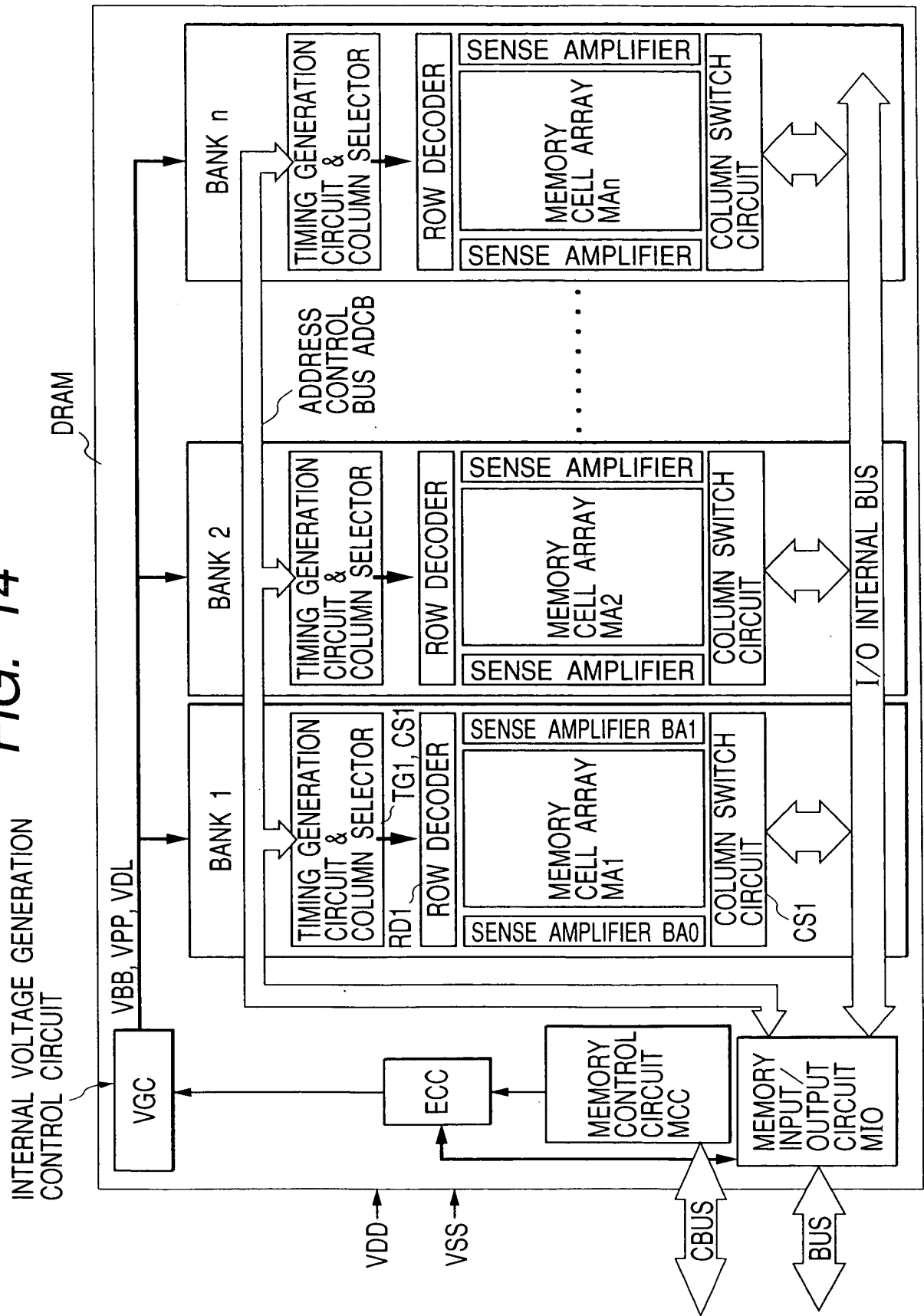


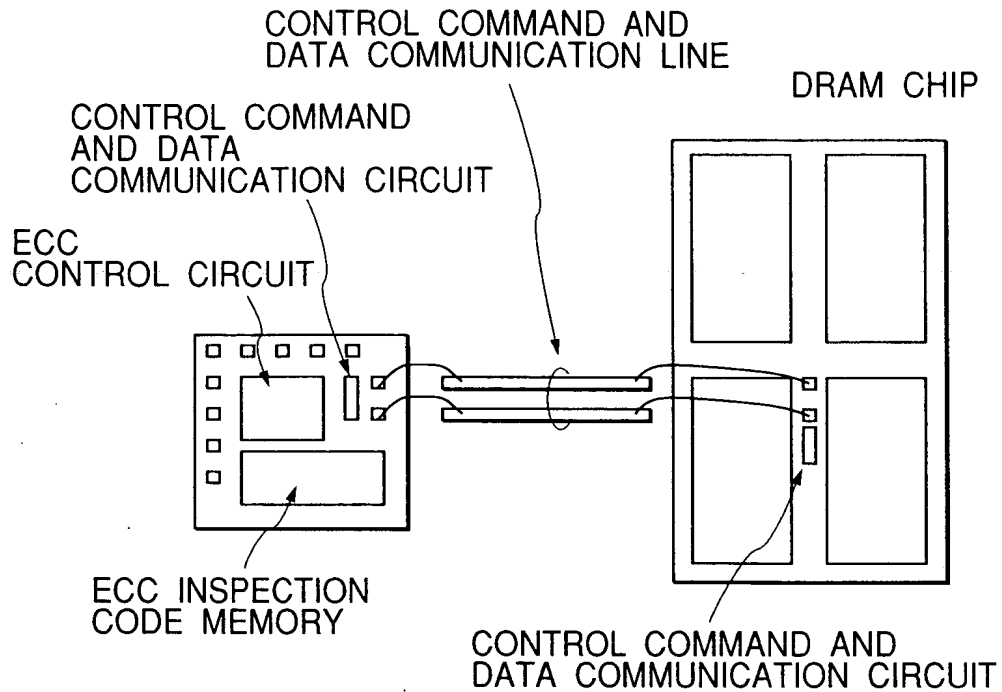
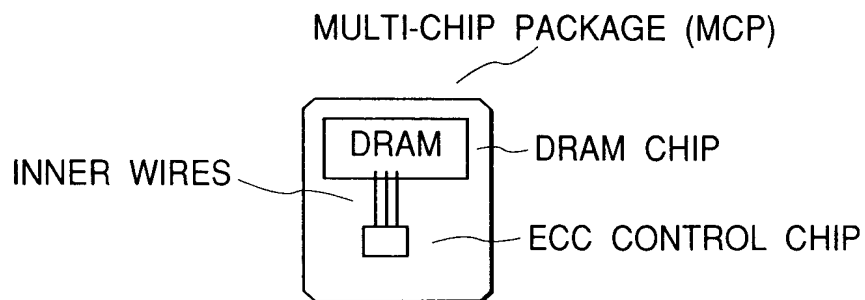
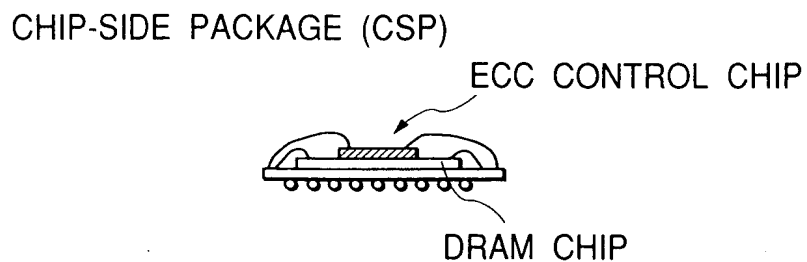
FIG. 15**FIG. 16(A)****FIG. 16(B)**

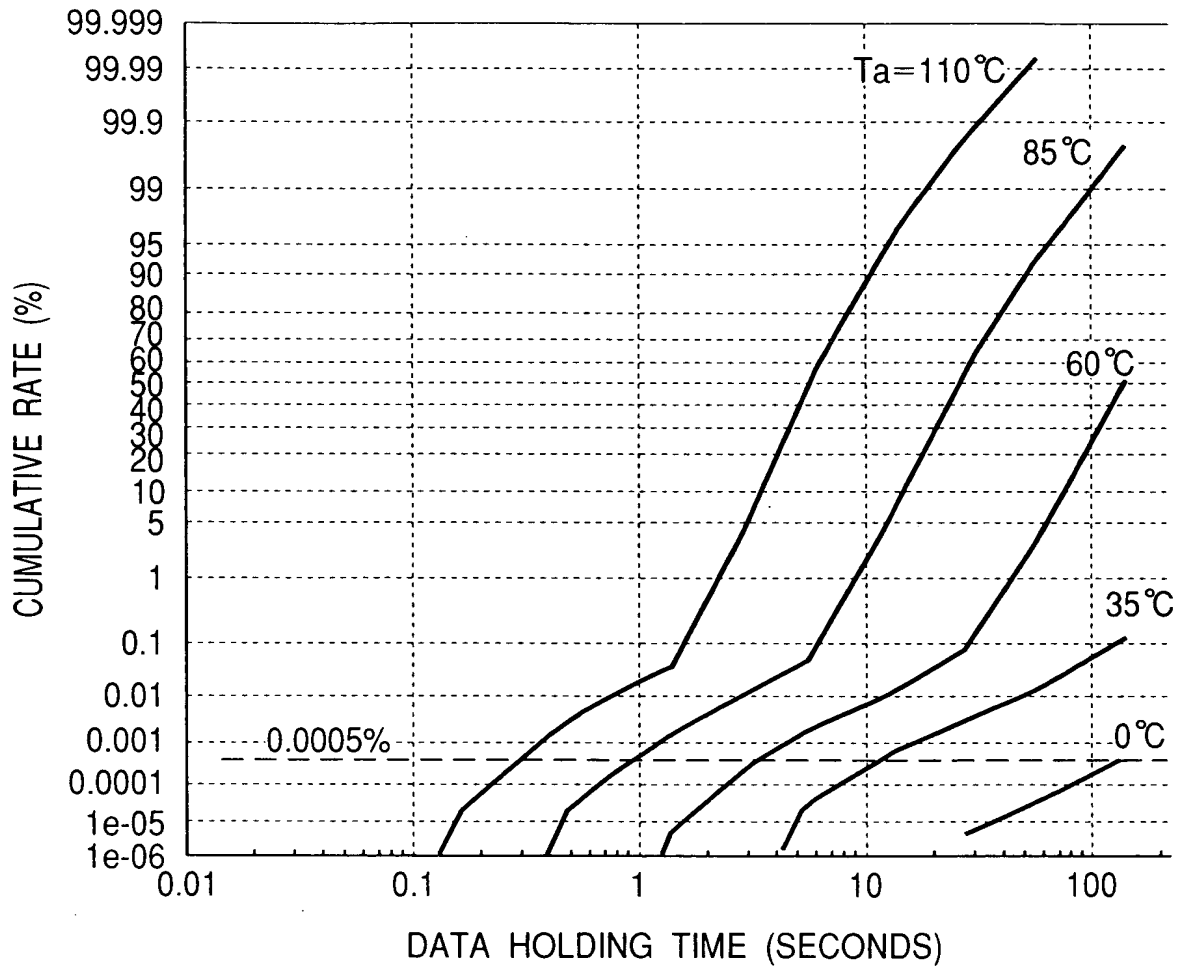
FIG. 17

FIG. 18